



WS2814A

Single-line 256-level grayscale four-channel constant current LED driver IC

Main feature

- The R, G, B and W output ports can withstand 20V, and the DIN port can withstand 9V.
- The chip has a built-in voltage regulator. For power supplies below 24V, only a resistor needs to be connected to the IC VDD pin, without an external voltage regulator.
- Built-in signal shaping circuit. After any IC receives a signal, it undergoes waveform shaping before outputting it, ensuring that line waveform distortion will not accumulate.
- Built-in power-on reset and power-off reset circuits.
- The PWM control end can achieve 256-level adjustment, with a scanning frequency of 2KHz.
- The serial interface cascade interface can complete data reception and decoding through a signal line.
- The transmission distance between any two points does not exceed 2 meters without adding any circuit.
- The color of the light is highly consistent and cost-effective.
- When the refresh rate is 30 frames/second, the number of cascades is not less than 1024 points.
- The data transmission speed can reach 800Kbps.

Main application areas

- LED full-color luminous character light string , LED full-color soft light bar, hard light bar,LED guardrail tube.
- LED point light source, LED pixel screen,LED special-shaped screen.

Product Overview

WS2814A is a four-channel LED drive control dedicated circuit. The chip contains an intelligent digital interface data latch signal shaping and amplification drive circuit, a high-precision internal oscillator and a 20V high-voltage programmable constant current output driver and high precision constant current control module, effectively ensure that the color of the pixel point light on the drive circuit is highly consistent.

The chip adopts a single-line return-to-zero code communication method. After the chip is powered on and reset, the DIN end receives the data transmitted from the controller. The first 32-bit data sent is extracted by the first chip and sent to the data latch inside the chip. The remaining data is shaped and amplified by the internal shaping processing circuit and then forwarded to the next cascade chip through the DO port. After each chip is transmitted, the signal is reduced by 32 bits. The chip adopts automatic shaping and forwarding technology, so that the number of cascades of the chip is not limited by signal transmission, but only by the signal transmission speed requirements.

The data latch inside the chip generates different duty cycle control signals at the OTR, OUTG, OUTB and OUTW control terminals according to the received 32-bit data. When waiting for the RESET signal to be input at the DIN terminal, all chips will synchronously send the received data to each segment. The chip will re-receive the data after the signal ends. After receiving the initial 32-bit data, it will forward the data port through the DO port. Before the chip receives the RESET code, the original output of the OTR, OUTG, OUTB and OUTW pins remains unchanged. After receiving a low-level RESET code of more than 280 μ s, the chip will output the 32-bit PWM data pulse width just received to the OTR, OUTG, OUTB and OUTW pins.

SOP8 and FSOP8 packages are available

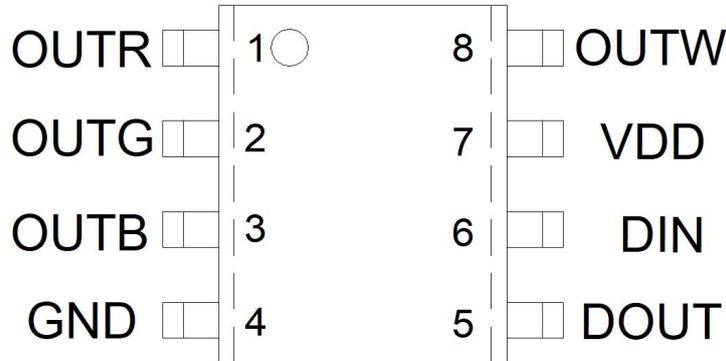


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Pin Configuration



Pin function

Pin number	Pin Symbols	Pin name	Function description
1	OUTR	LED driver output	RED (red) PWM control output
2	OUTG	LED driver output	GREEN (green) PWM control output
3	OUTB	LED driver output	BLUE (blue) PWM control output
4	GND	Ground	Signal ground and power ground
5	DOUT	Data output	Display data cascade output
6	DIN	Data input	Display data input
7	VDD	Logic Power Supply	IC power supply
8	OUTW	LED driver output	WHITE (white) PWM control output

Maximum rating (T_A=25°C, V_{SS}=0V)

Parameter	Symbol	Range	Unit
Logic supply voltage	V _{DD}	+3.7~+5.3	V
Logic input voltage	V _I	V _{DD} -0.7~V _{DD} +0.7	V
R、G、B、W output port withstand voltage	V _{out}	20	V
Operating temperature	T _{opt}	-40~+85	°C
Storage temperature	T _{stg}	-40~+150	°C
Static immunity	ESD	≧4	KV



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Electrical parameters ($T_A=25^\circ\text{C}$, $V_{DD}=4.5\sim 5.5\text{V}$, $V_{SS}=0\text{V}$)

Parameter	Symbol	Minimum	Typical	Maximum	Unit	Test Conditions
R、G、B、W low level output current	I_{OL}	15.5	16.5	17.5	mA	
Low level output current	I_{dout}	10	—	—	mA	$V_o=0.4\text{V}$, D_{OUT}
Signal input current	I_I	—	—	± 1	μA	$V_I=V_{DD}/V_{SS}$
High level input	V_{IH}	$0.7V_{DD}$	—	—	V	D_{IN}
Low level input	V_{IL}	—	—	$0.3 V_{DD}$	V	D_{IN}
Hysteresis voltage	V_H	—	0.35	—	V	D_{IN}

Switching characteristics ($T_A=25^\circ\text{C}$, $V_{DD}=4.5\sim 5.5\text{V}$, $V_{SS}=0\text{V}$)

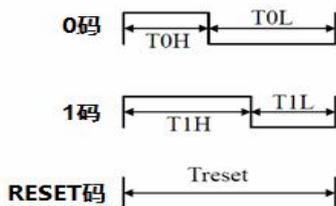
Parameter	Symbol	Minimum	Typical	Maximum	Unit	Test Conditions
Transmission delay time	t_{PLZ}	—	—	300	ns	$CL=15\text{pF}$, $D_{IN}\rightarrow D_{OUT}$, $RL=10\text{K}\Omega$
Fall time	t_{THZ}	—	—	120	μs	$CL=300\text{pF}$, $OUTR/OUTG/OUTB/OUTW$
Fall time Data transfer rate	F_{MAX}	600	—	—	Kbps	Duty cycle 50%
Input capacitance	C_I	—	—	15	pF	—

Data transmission time

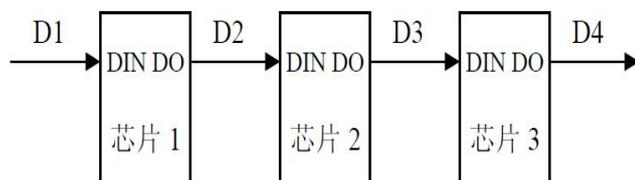
T_{0H}	0 code, High level time	220ns~380ns
T_{1H}	1 code, High level time	580ns~1 μs
T_{0L}	0 code, Low level time	580ns~1 μs
T_{1L}	1 code, Low level time	580ns~1 μs
RES	Frame unit, low level time	280 μs 以上
T_{DATA}	data cycle	$\geq 1.25\mu\text{s}$

Timing waveform

Input code type:



Connection method:

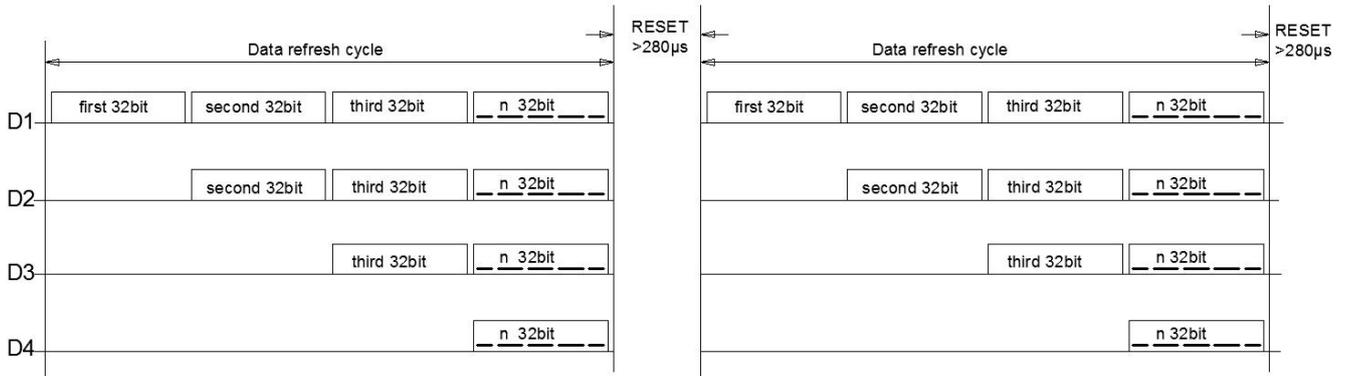




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Data transmission method



Note: D1 is the data sent by the MCU, and D2, D3, and D4 are the data automatically shaped and forwarded by the cascade circuit.

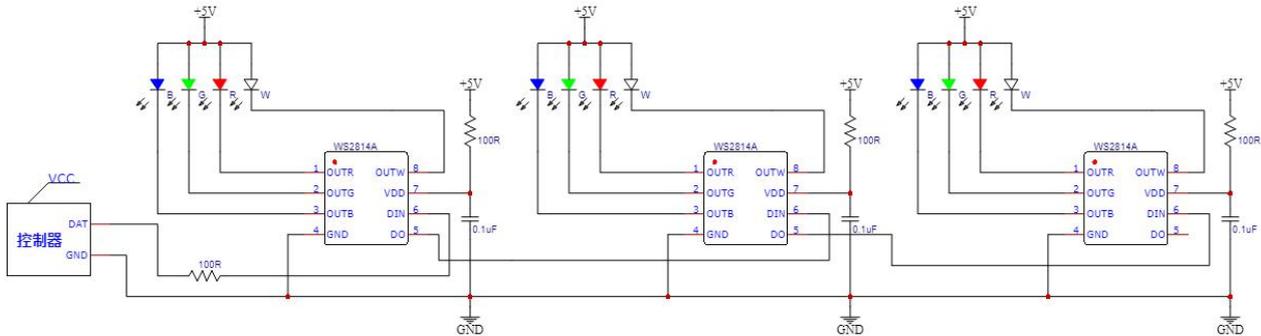
32-bit data structure

W7	W6	W5	W4	W3	W2	W1	W0	R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
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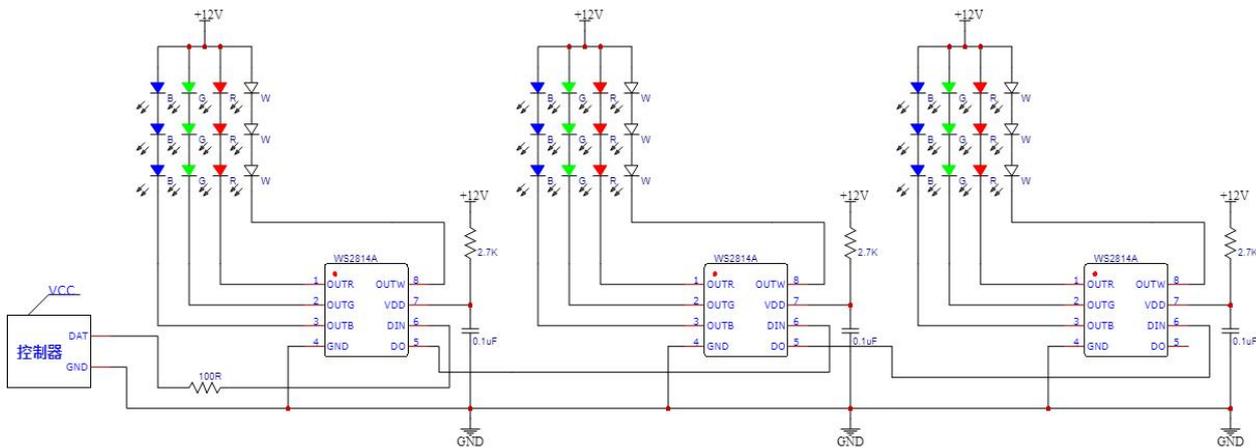
Note: The high bit is sent first, and the data is sent in the order of RGBW

Typical application circuit

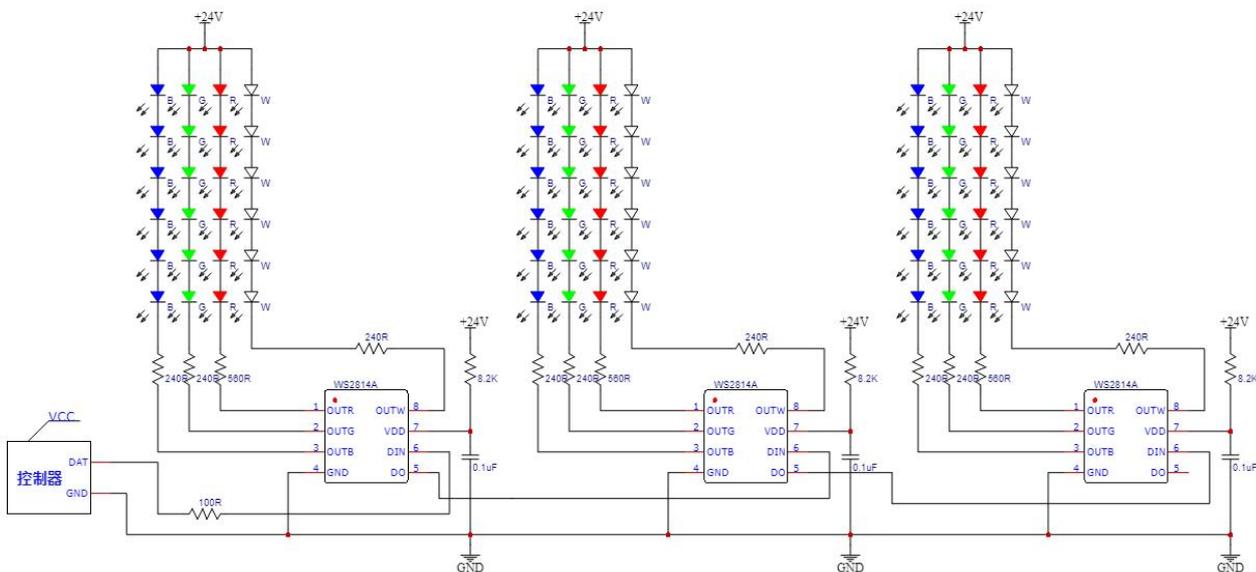
1. 5V power supply application reference circuit (each channel has 1 LED):



2. 12V power supply application reference circuit (each channel with 3 LEDs):



3. 24V power supply application reference circuit (each channel with 6 LEDs):





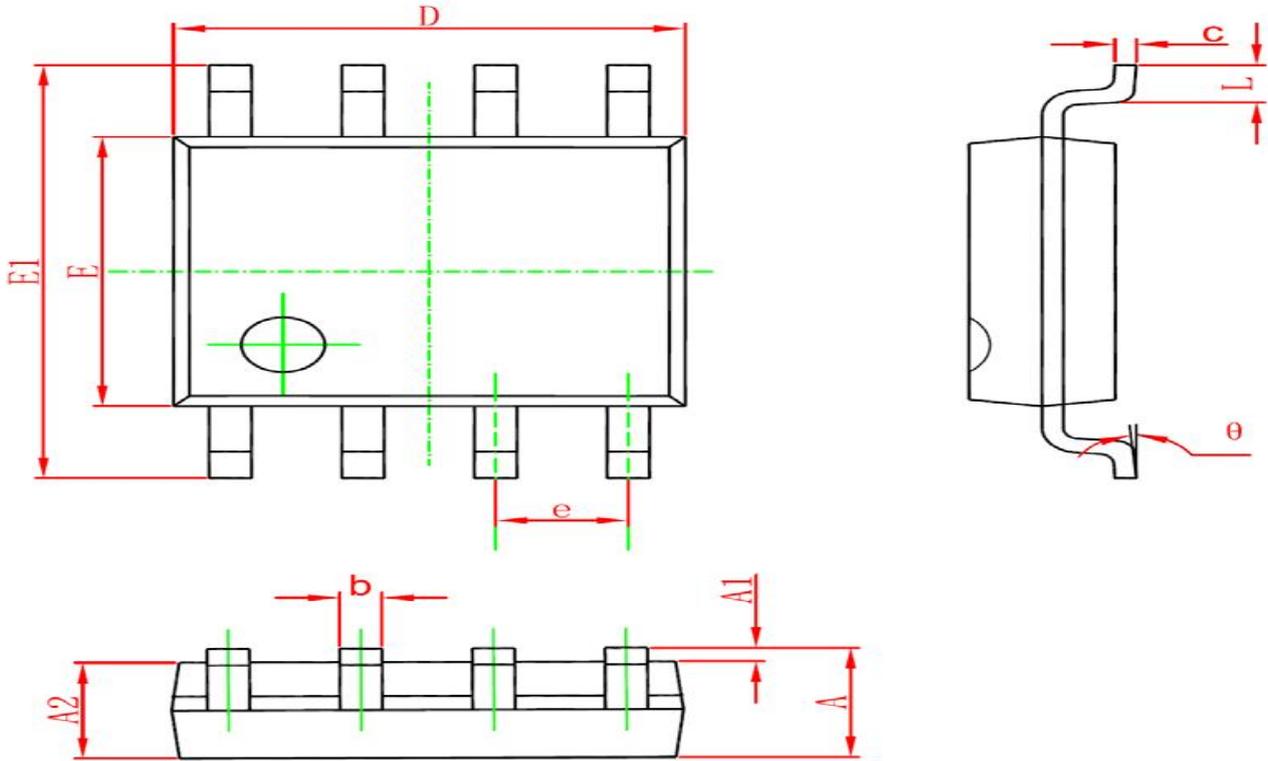
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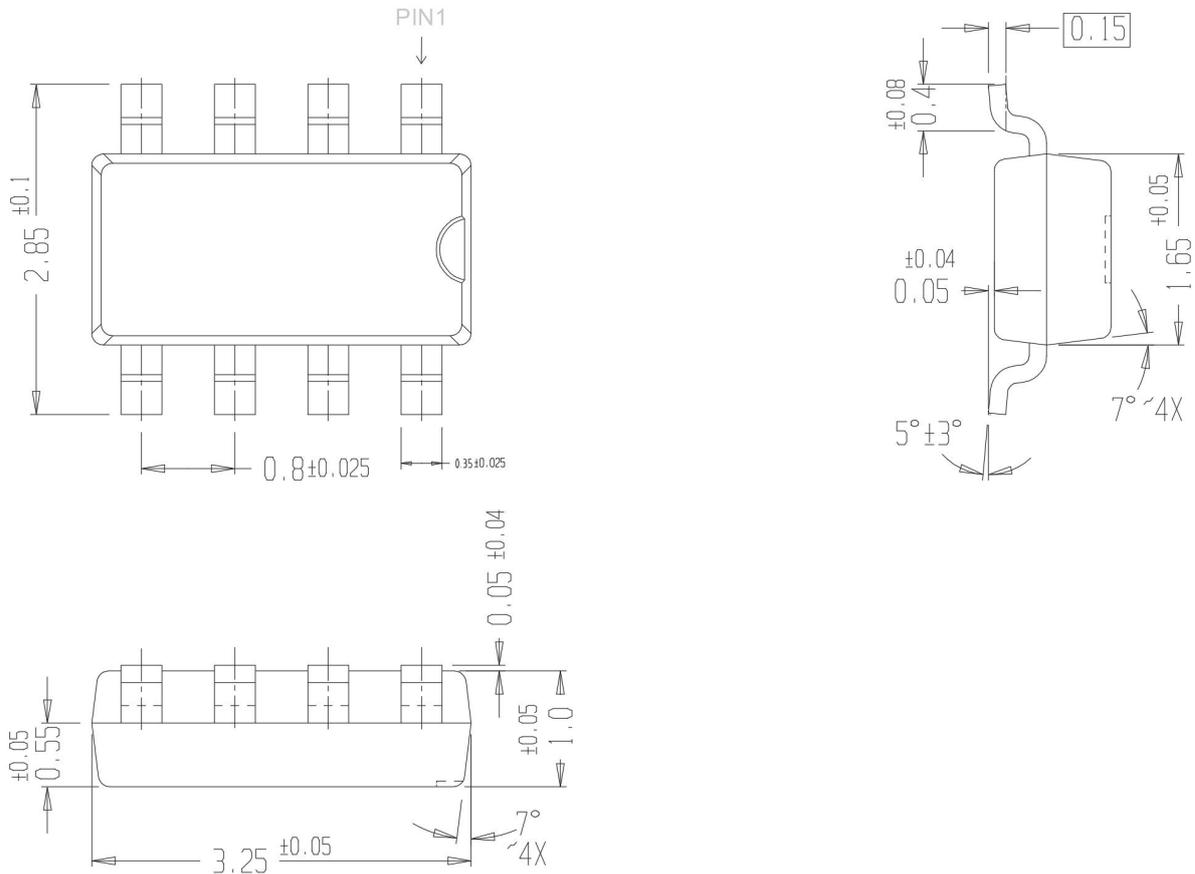
Package diagram and parameters

- SOP8 package



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270		0.050	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°

- FSOP8 package



File Change Log

Version number	Status	Summary of changes	Revision date	Revised by	Approver
V1.0	N	New	20190410	Dong Le	Shen Jinguo
V1.1	M	Detail parameter correction	20210118	Dong Le	Shen Jinguo
V1.2	M	Modify typical application circuits	20211125	Xie Yanfan	Yu Xinghui
V1.3	M	Add ESD item	20220712	Hu Jin	Yu Xinghui
V1.4	A	Add FSOP8 package	2023/3/28	Hu Jin	Yu Xinghui

Note: The initial version number is V1.0; after each revision is approved, the version number is incremented by "0.1"; Status includes: N--New, A--Add, M--Modify, D--Delete.